IRIS
CUSTOMER
SUPPORT
BULLETIN

May 10, 1984

POINT 4 Data Corporation

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TO: IRIS Subscription Service Customers

FROM: IRIS Customer Support

DATE: May 10, 1984

SUBJ: CUSTOMER SUPPORT BULLETIN

I.   IRIS SYSTEM UPDATES

    A.   CUSTOMER SUPPORT NEWS

    B.   SOFTWARE CHANGE ORDERS FOR R8.2 SYSTEMS - NONE

    C.   SOFTWARE CHANGE ORDERS FOR R7.5 SYSTEMS - NONE

II. DOCUMENTATION UPDATES

    A.   IRIS R8 DOCUMENTATION

        1.   Reissued Tech Memos:

            a.   Half-inch STREAMER Utility, Rev 02

    B.   PUBLICATIONS BULLETIN

POINT 4 DATA CORPORATION

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CUSTOMER SUPPORT NEWS

May 10, 1984

by

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Director, IRIS Customer Support

In This Month's Bulletin

This month's bulletin includes a revised tech memo on using the STREAMER half-inch streaming tape utility.

The original memo issued in last month's bulletin gave an incorrect pin number for jumpering on the Rianda 2200 Controller Board. This error has been corrected in the revised STREAMER Tech Memo in this month's bulletin.

IRIS 8.2C

IRIS 8.2C will soon be available for first shipments. IRIS 8.2C will contain a new driver $SYSMAP and other system changes necessary to use the new MARK 9 CPU with mapped memory.

Once you get IRIS 8.2C, it is very simple to use the map. Enable $SYSMAP and set two cells in CONFIG, as described in documentation you will receive with 8.2C. Do a new IPL and run MAPACTIVATE.

No other configuring is needed. You must have a Pico-N which is enabled to use mapped memory or MAPACTIVATE will give an error message.

The MARK 9 map may be used with or without an LCM. In either case, the effect of having partitions in the MARK 9 mapped memory is the same as having that many memory-resident user partitions (since a new partition can map in almost instantaneously). If there are as many partitions in mapped memory as there are users, all swapping overhead will be (virtually) eliminated. Even if there are more users than partitions in mapped memory, swapping overhead will be reduced drastically.

If there is no LCM, the MARK 9 map can be used both for partitions and as a dynamic buffer pool cache.

If there is an LCM, the MARK 9 will be used for partitions only and the LCM for static allocation and dynamic buffer pool cache.

For systems with large numbers of users and a large LCM, the LCM can be used for active files as well as for static allocation and as a dynamic buffer pool cache. Thus, the MARK 9 map reduces most of the swapping overhead and the LCM can be used to handle the rest. This is only advantageous in the largest systems where all the ports are in use all the time.

Using MARK 8 Under IRIS R8

To use $MK8 under IRIS R8, you must have Revision 8.2B (or any later revision). If you are using 8.2B, check your IRIS 8.2B Release Notes for SCO MK882-03 ("BASIC returns unexpectedly to SCOPE when running $MK8".) This SCO was not available in time for inclusion on the 8.2B templates, so you must apply that patch yourself from the 8.2B Release Notes before you enable $MK8.

On all future revisions of IRIS, beginning with IRIS 8.2C, $MK8 will be renamed $EIS (Extended Instruction Set), and will already contain SCO MK882-03.

$MMUX Patches for TYPIST Users

For MARK 3 Systems:

A very important patch, SCO MMUXM382-02, was issued in the Feb '84 Customer Support Bulletin for all IRIS 8.2 and 8.2A Systems. This patch was not available in time for inclusion in the IRIS 8.2B templates, so the same patch was issued in the IRIS 8.2B Release Notes as SCO MMUXM382-03. If you have not applied this patch, your port could become hung up at the first input in TYPIST.

For MARK 5 or MARK 8-type Systems:

The following problem can occur: Fast typing of control characters in TYPIST can cause echo to be lost and garbage to appear when subsequent control keys are pressed. CTRL-C will cure the condition but will cause exit from TYPIST.

The patch to prevent this problem will be released as SCO MMUXM582-05. This patch has been tested but the paper work is not yet complete. Here is a pre-release of that patch that can be applied to $MMUX on any MARK 5 or 8-type System under IRIS 8.2, 8.2A, or 8.2B.

Location	New Contents	Old Contents
33053	LDA 1,27	24025