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REVISIONS				
REV.	DESCRIPTION	DRFTG.	APP BY	DATE
01	ECO # 889			4-72

IN-OUT BUS SIGNAL CONNECTIONS

A THRU AF COMPONENT SIDE

① THRU ⑤⓪ WIREWRAP SIDE

Pin	Signal	Wirewrap
A	GND	①
B	+5V	
C	MSKO	A38
D	INTA	A40
E	DATIB	A42
F	DATIA	A44
H	DS3	A46
J	DATOC	A48
K	CLR	A50
L	STRT	A52
M	DATIC	A54
N	DATOB	A56
P	DATOA	A58
R	DCHA	A60
S	DS4	A62
T	DS5	A64
U	DS2	A66
V	DS1	A68
W	IORST	A70
X	DS0	A72
Y	IOPLS	A74
Z	SELD	A80
a	SELB	A82
b	DCHP OUT	A93
c	INTP OUT	A95
d	DCHMO	B17
e	DCHMI	B21
f	INTR	B29
h	DCHO	B33
j	DCHR	B35
k	DCHI	B37
l	OVFLO	B39
m	RQENB	B41
n	DATA7	B55
p	DATA14	B56
r	DATA5	B57
s	DATA11	B58
t	DATA12	B59
u	DATA8	B60
v	DATA4	B61
w	DATA0	B62
x	DATA9	B63
y	DATA13	B64
z	DATA1	B65
AA	DATA15	B66
AB	DATA3	B73
AC	DATA10	B75
AD	DATA2	B82
AE	DATA6	B95
AF	GND	⑤⓪

I/O 10

Pins	Left ODD	Left EVEN	Right ODD	Right EVEN
1/2	GND	GND	GND	GND
3/4	+5V	+5V	+5V	+5V
5/6		-5V		
7/8				
9/10		+V INH		
11/12				
13/14				
15/16				
17/18			DCHMO	
19/20				
21/22			DCHMI	
23/24				
25/26				
27/28				
29/30			INTR	
31/32				
33/34	GND	GND	DCHO	
35/36			DCHR	
37/38		MSKO	DCHI	
39/40		INTA	OVFLO	
41/42		DATIB	RQENB	
43/44		DATIA		
45/46		DS3		+V INH
47/48		DATOC		
49/50		CLR		GND
51/52		STRT		
53/54		DATIC		
55/56		DATOB	DATA7	DATA14
57/58		DATOA	DATA5	DATA11
59/60		DCHA	DATA12	DATA8
61/62		DS4	DATA4	DATA0
63/64		DS5	DATA9	DATA13
65/66		DS2	DATA1	DATA15
67/68		DS1		
69/70		IORST		
71/72		DS0		
73/74		IOPLS	DATA3	
75/76			DATA10	
77/78				
79/80		SELD		
81/82		SELB	-5V	DATA2
83/84				+V INH
85/86				
87/88				
89/90			GND	
91/92				GND
93/94	DCHP OUT	DCHP IN		
95/96	INTP OUT	INTP IN	DATA6	
97/98	+5V	+5V	+5V	+5V
99/100	GND	GND	GND	GND

I/O 9

Pins	Left ODD	Left EVEN	Right ODD	Right EVEN
1/2	GND	GND	GND	GND
3/4	+5V	+5V	+5V	+5V
5/6		-5V		PIN-29
7/8				
9/10		+V INH		
11/12			PIN-30	
13/14			PIN-31	
15/16			PIN-32	
17/18			DCHMO	
19/20			PIN-33	
21/22			DCHMI	
23/24			PIN-34	
25/26			PIN-35	
27/28			PIN-36	
29/30			INTR	
31/32			PIN-37	
33/34	GND	GND	DCHO	PIN-38
35/36			DCHR	PIN-39
37/38		MSKO	DCHI	PIN-40
39/40		INTA	OVFLO	PIN-41
41/42		DATIB	RQENB	
43/44		DATIA		
45/46		DS3		+V INH
47/48	PIN-17	DATOC		PIN42
49/50	PIN-18	CLR	PIN-43	GND
51/52		STRT	PIN-44	PIN 45
53/54		DATIC	PIN-46	PIN 47
55/56		DATOB	DATA7	DATA14
57/58	PIN-16	DATOA	DATA5	DATA11
59/60	PIN-15	DCHA	DATA12	DATA8
61/62	PIN-14	DS4	DATA4	DATA0
63/64	PIN-13	DS5	DATA9	DATA13
65/66	PIN-12	DS2	DATA1	DATA15
67/68	PIN-11	DS1	PIN-48	
69/70	PIN-10	IORST	PIN-49	
71/72	PIN-9	DS0		
73/74	PIN-8	IOPLS	DATA3	
75/76	PIN-7	PIN-6	DATA10	
77/78	PIN-5	PIN4		
79/80	PIN-19	SELD		
81/82	PIN-20	SELB	-5V	DATA2
83/84	PIN-22	PIN-21		+V INH
85/86	PIN-24	PIN-23		
87/88	PIN-26	PIN-25		
89/90	PIN-27	PIN-28	GND	
91/92	PIN-3	PIN-2		GND
93/94	DCHP OUT	DCHP IN		
95/96	INTP OUT	INTP IN	DATA6	
97/98	+5V	+5V	+5V	+5V
99/100	GND	GND	GND	GND

MEMORY OR I/O 8

Pins	Left ODD	Left EVEN	Right ODD	Right EVEN
1/2	GND	GND	GND	GND
3/4	+5V	+5V	+5V	+5V
5/6	RINH0	-5V	MBO11	
7/8	RINH1	INH GND	MA LOAD	MBO10
9/10	RINH2	+V INH	MBO9	
11/12	RINH3			MBO8
13/14	RINH4	INH GND		MBO7
15/16	RINH5	INH GND		MBO6
17/18	RINH7	RINH6	DCHMO	MEM15
19/20	RINH8	INH GND		STROBE
21/22	RINH11	INH GND	DCHMI	MEM6
23/24	RINH10	RINH9		MEM7
25/26	RINH13	INH GND		MEM5
27/28	RINH15	RINH12		MEM4
29/30	RINH14	INH GND	INTR	INHIBIT
31/32	INH GND			MBO5
33/34	GND	GND	DCHO	
35/36	MEM13	MEM12	DCHR	
37/38	MBO13	MSKO	DCHI	
39/40	MBO12	INTA	OVFLO	
41/42	MBO15	DATIB	RQENB	MBO4
43/44	MBO14	DATIA	MBO3	MBO2
45/46	MEM10	DS3	INHIBIT TRANSMISSION	+V INH
47/48		DATOC	MEM2	
49/50		CLR		GND
51/52	MEM11	STRT		
53/54	MEM9	DATIC		
55/56	MEM8	DATOB	DATA7	DATA14
57/58		DATOA	DATA5	DATA11
59/60		DCHA	DATA12	DATA8
61/62		DS4	DATA4	DATA0
63/64		DS5	DATA9	DATA13
65/66		DS2	DATA1	DATA15
67/68		DS1		MEM3
69/70		IORST		MEM1
71/72		DS0	MEM0	RELOAD DISABLE
73/74		IOPLS	DATA3	MB LOAD
75/76			DATA10	MEM14
77/78			MBO1	ROS
79/80		SELD	MBO0	EXTERNAL SELECT
81/82		SELB	-5V	DATA2
83/84			READ IO	+V INH
85/86			INHIBIT SELECT	MB CLEAR
87/88			READ 1	DRIVE IO
89/90			GND	READ2
91/92				GND
93/94	DCHP OUT	DCHP IN		
95/96	INTP OUT	INTP IN	DATA6	
97/98	+5V	+5V	+5V	+5V
99/100	GND	GND	GND	GND

SLOTS 4 THRU 7 ARE TYPICAL TO SLOT 8

MEMORY OR I/O 3

Pins	Left ODD	Left EVEN	Right ODD	Right EVEN
1/2	GND	GND	GND	GND
3/4	+5V	+5V	+5V	+5V
5/6	RINH0	-5V	MBO11	
7/8	RINH1	INH GND	MA LOAD	MBO10
9/10	RINH2	+V INH	MBO9	
11/12	RINH3	PTR-B	PTP-E	MBO8
13/14	RINH4	INH GND	PTP-4	MBO7
15/16	RINH5	INH GND	PTP-C	MBO6
17/18	RINH7	RINH6	DCHMO	MEM15
19/20	RINH8	INH GND		STROBE
21/22	RINH11	INH GND	DCHMI	MEM6
23/24	RINH10	RINH9	PTP-3	MEM7
25/26	RINH13	INH GND	PTP-6	MEM5
27/28	RINH15	RINH12	PTP-B	MEM4
29/30	RINH14	INH GND	INTR	INHIBIT
31/32	INH GND			MBO5
33/34	GND	GND	DCHO	PTP-F
35/36	MEM13	MEM12	DCHR	PTP-7
37/38	MBO13	MSKO	DCHI	PTP-H
39/40	MBO12	INTA	OVFLO	PTP-8
41/42	MBO15	DATIB	RQENB	MBO4
43/44	MBO14	DATIA	MBO3	MBO2
45/46	MEM10	DS3	INHIBIT TRANSMISSION	+V INH
47/48	PTR-C	DATOC	MEM2	PTP-J
49/50	PTR-2	CLR	PTP-9	GND
51/52	MEM11	STRT	PTP-K	EIA-8
53/54	MEM9	DATIC	EIA-J	EIA-6
55/56	MEM8	DATOB	DATA7	DATA14
57/58	PTR-4	DATOA	DATA5	DATA11
59/60	PTR-D	DCHA	DATA12	DATA8
61/62	PTR-5	DS4	DATA4	DATA0
63/64	PTR-6	DS5	DATA9	DATA13
65/66	PTR-H	DS2	DATA1	DATA15
67/68	PTR-7	DS1	PTP-2	MEM3
69/70	PTR-J	IORST	EIA-3	MEM1
71/72	PTR-8	DS0	MEM0	RELOAD DISABLE
73/74	PTR-K	IOPLS	DATA3	MB LOAD
75/76	PTR-9		DATA10	MEM14
77/78	PTR-L	EIA-4	MBO1	ROS
79/80	EIA-L	SELD	MBO0	EXTERNAL SELECT
81/82	EIA-5	SELB	-5V	DATA2
83/84			READ IO	+V INH
85/86	EIA-2		INHIBIT SELECT	MB CLEAR
87/88	EIA-B		READ 1	DRIVE IO
89/90			GND	READ 2
91/92				GND
93/94	DCHP OUT	DCHP IN		
95/96	INTP OUT	INTP IN	DATA6	
97/98	+5V	+5V	+5V	+5V
99/100	GND	GND	GND	GND

MEMORY OR 1220 OPTION 2

Pins	Left ODD	Left EVEN	Right ODD	Right EVEN
1/2	GND	GND	GND	GND
3/4	+5V	+5V	+5V	+5V
5/6	RINH0	-5V	MBO11	WHOA
7/8	RINH1	INH GND	MA LOAD	MBO10
9/10	RINH2	+V INH	MBO9	
11/12	RINH3			MBO8
13/14	RINH4	INH GND		MBO7
15/16	RINH5	INH GND		MBO6
17/18	RINH7	RINH6	DCHMO	MEM15
19/20	RINH8	INH GND		STROBE
21/22	RINH11	INH GND	DCHMI	MEM6
23/24	RINH10	RINH9		MEM7
25/26	RINH13	INH GND		MEM5
27/28	RINH15	RINH12		MEM4
29/30	RINH14	INH GND	INTR	INHIBIT
31/32	INH GND			MBO5
33/34	GND	GND	DCHO	
35/36	MEM13	MEM12	DCHR	
37/38	MBO13	MSKO	DCHI	
39/40	MBO12	INTA	OVFLO	
41/42	MBO15	DATIB	RQENB	MBO4
43/44	MBO14	DATIA	MBO3	MBO2
45/46	MEM10	DS3	INHIBIT TRANSMISSION	+V INH
47/48	EXT-LOAD	DATOC	MEM2	CLK FLOP
49/50	FETCH	CLR	EXT LOAD	GND
51/52	MEM11	STRT		
53/54	MEM9	DATIC		
55/56	MEM8	DATOB	DATA7	DATA14
57/58		DATOA	DATA5	DATA11
59/60		DCHA	DATA12	DATA8
61/62		DS4	DATA4	DATA0
63/64		DS5	DATA9	DATA13
65/66		DS2	DATA1	DATA15
67/68		DS1		MEM3
69/70		IORST		MEM1
71/72		DS0	MEM0	RELOAD DISABLE
73/74	LOAD IR	IOPLS	DATA3	MB LOAD
75/76			DATA10	MEM14
77/78	LOAD AC		MBO1	ROS
79/80		SELD	MBO0	EXTERNAL SELECT
81/82		SELB	-5V	DATA2
83/84			READ IO	+V INH
85/86	FORCE LOAD IR		INHIBIT SELECT	MB CLEAR
87/88	MD SEL 1		READ 1	DRIVE IO
89/90	WAS JSR		GND	READ 2
91/92	DIV	TEST	SHIFT3	GND
93/94			SHIFT2	SHIFT0
95/96			DATA6	SHIFT1
97/98	+5V	+5V	+5V	+5V
99/100	GND	GND	GND	GND

CPU 1

Pins	Left ODD	Left EVEN	Right ODD	Right EVEN
1/2	GND	GND	GND	GND
3/4	+5V	+5V	+5V	+5V
5/6	POWER FAIL	-5V	MBO11	WHOA
7/8		+5 OK	MA LOAD	MBO10
9/10	MEM OK	+V INH	MBO9	
11/12	EXEC	DEFER		MBO8
13/14	FETCH	RUN		MBO7
15/16	CARRY	ION		MBO6
17/18	ISTP		DCHMO	MEM15
19/20	PL	MSTP		STROBE
21/22		CON INST	DCHMI	MEM6
23/24				MEM7
25/26	CONT+ISTP + MSTP			MEM5
27/28	CONRQ	CONT DATA		MEM4
29/30		RST	INTR	INHIBIT
31/32	STOP	RESTART ENABLE		MBO5
33/34	GND	GND	DCHO	
35/36	MEM13	MEM12	DCHR	
37/38	MBO13	MSKO	DCHI	
39/40	MBO12	INTA	OVFLO	
41/42	MBO15	DATIB	RQENB	MBO4
43/44	MBO14	DATIA	MBO3	MBO2
45/46	MEM10	DS3	INHIBIT TRANSMISSION	
47/48	EXT-LOAD	DATOC	MEM2	CLK FLOP
49/50	FETCH	CLR	EXT LOAD	GND
51/52	MEM11	STRT		
53/54	MEM9	DATIC		
55/56	MEM8	DATOB	DATA7	DATA14
57/58		DATOA	DATA5	DATA11
59/60		DCHA	DATA12	DATA8
61/62		DS4	DATA4	DATA0
63/64		DS5	DATA9	DATA13
65/66		DS2	DATA1	DATA15
67/68		DS1		MEM3
69/70		IORST	SKIP	MEM1
71/72		DS0	MEM0	RELOAD DISABLE
73/74	LOAD IR	IOPLS	DATA3	MB LOAD
75/76			DATA10	MEM14
77/78	LOAD AC		MBO1	
79/80		SELD	MBO0	
81/82		SELB		DATA2
83/84			READ IO	
85/86	FORCE LOAD IR			MB CLEAR
87/88	MD SEL 1		READ 1	DRIVE IO
89/90	WAS JSR		GND	READ2
91/92	DIV	TEST	SHIFT3	GND
93/94			SHIFT2	SHIFT0
95/96			DATA6	SHIFT1
97/98	+5V	+5V	+5V	+5V
99/100	GND	GND	GND	GND

4010 TELETYPE CONNECTION SLOT 3

Pins	84	86	88	90	92	94	96	98	100
Top	+V	TT OUT	DATA IN STOP	RD RUN				+5V	GND
Bottom								+5V	GND

WIRE WRAP PINS: 3A6 -5V; 3B69 TT IN

QTY	DESCRIPTION		PART NO
DRAWN PERRYMAN	1-5-72	DATA GENERAL CORPORATION SOUTHBORO, MASSACHUSETTS 01772	
CHECKED		TITLE	
ENGINEER		BACK PANEL NOVA 1220	
APPROVED			
FIRST USED ON 107-000128			
SCALE		SIZE D / CODE 001 / DRAWING NUMBER 000208	REV 01

UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES XX ± ± XXX ± ± PARTS TO BE FREE FROM BURRS BREAK ALL EDGES 010

UNLESS OTHERWISE SPECIFIED - ALL MACH. SURFACES

MATERIAL

FINISH