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REVISIONS				
REV.	DESCRIPTION	DRFTG.	APP BY	DATE
01	REV. PER MKD PRINT	RAS		3/19/71
02	ECO 889	MJD		5/72
03	ECO # 1639	R.W.C.		6-4-73

A

Slots: 7 = 4K MEM OR I/O 7; 6 = 4K MEM OR I/O 6; 5 = 4K MEM OR I/O 5; 4 = 4K MEM OR I/O 4K 4; 3 = 4K MEM OR I/O 3; 2 = 4K MEM OR 1200 OPTION 2; 1 = CPU 1. Odd pins are in the ODD rows and even pins in the EVEN rows.

Pin	7	6	5	4	3	2	1 (CPU)
1	GND	GND	GND	GND	GND	GND	GND
2	GND	GND	GND	GND	GND	GND	GND
3	+5V	+5V	+5V	+5V	+5V	+5V	+5V
4	+5V	+5V	+5V	+5V	+5V	+5V	+5V
5	RINH 0	RINH 0	RINH 0	RINHO	RINH 0	RINHO	POWER FAIL
6	-5V	-5V	-5V	-5V	-5V	-5V	
7	RINH 1	RINH 1	RINH 1	RINH1	RINH 1	RINH1	
8	INH GND	INH GND	INH GND	INH GND	INH GND	INH GND	+5 OK
9	RINH 2	RINH 2	RINH 2	RINH 2	RINH 2	RINH 2	MEM OK
10	+VINH	+VINH	+VINH	+VINH	+VINH	+VINH	
11	RINH 3	RINH 3	RINH 3	RINH 3	RINH 3	RINH 3	EXEC
12						N/C	DEFER
13	RINH 4	RINH 4	RINH 4	RINH 4	RINH 4	RINH 4	FETCH
14	INH GND	INH GND	INH GND	INH GND	INH GND	INH GND	RUN
15	RINH 5	RINH 5	RINH 5	RIN H5	RINH 5	RIN H5	CARRY
16	INH GND	INH GND	INH GND	INH GND	INH GND	INH GND	ION
17	RINH 7	RINH 7	RINH 7	RIN H7	RINH 7	RIN H7	ISTP
18	RINH 6	RINH 6	RINH 6	RINH 6	RINH 6	RINH 6	
19	RINH 8	RINH 8	RINH 8	RIN H8	RINH 8	RIN H8	PL
20	INH GND	INH GND	INH GND	INH GND	INH GND	INH GND	MSTP
21	RINH 11	RINH 11	RINH 11	RINH11	RINH 11	RINH11	
22	INH GND	INH GND	INH GND	INH GND	INH GND	INH GND	CON INST
23	RINH 10	RINH 10	RINH 10	RINH10	RINH 10	RINH10	
24	RINH 9	RINH9	RINH9	RINH9	RINH9	RINH9	
25	RINH 13	RINH 13	RINH 13	RINH13	RINH 13	RINH13	CONT+ISTP+MSTP
26	INH GND	INH GND	INH GND	INH GND	INH GND	INH GND	
27	RINH 15	RINH 15	RINH 15	RINH15	RINH 15	RINH15	CON RQ
28	RINH 12	RINH 12	RINH 12	RINH12	RINH 12	RINH12	CONT DATA
29	RINH 14	RINH 14	RINH 14	RINH14	RIN H14	RINH14	
30	INH GND	INH GND	INH GND	INH GND	INH GND	INH GND	RST
31	INH GND	INH GND	INH GND	INH GND	INH GND	INH GND	STOP
32						N/C	RESTART ENABLE
33	GND	GND	GND	GND	GND	GND	GND
34	GND	GND	GND	GND	GND	GND	GND
35	MEM 13	MEM 13	MEM 13	MEM 13	MEM 13	MEM 13	MEM13
36	MEM 12	MEM 12	MEM 12	MEM 12	MEM 12	MEM 12	MEM12
37	MBO13	MBO13	MBO13	MBO13	MBO13	MBO13	MBO13
38	MSKO	MSKO	MSKO	MSKO	MSKO	MSKO	MSKO
39	MBO12	MBO12	MBO12	MBO12	MBO12	MBO12	MBO12
40	INTA	INTA	INTA	INTA	INTA	INTA	INTA
41	MBO15	MBO15	MBO15	MBO15	MBO15	MBO15	MBO15
42	DATIB	DATIB	DATIB	DATIB	DATIB	DATIB	DATIB
43	MBO14	MBO14	MBO14	MBO14	MBO14	MBO14	MBO14
44	DATIA	DATIA	DATIA	DATIA	DATIA	DATIA	DATIA
45	MEM 10	MEM 10	MEM 10	MEM 10	MEM 10	MEM 10	MEM10
46	DS3	DS3	DS3	DS3	DS3	DS3	DS3
47						EXT LOAD	EXT LOAD
48	DATOC	DATOC	DATOC	DATOC	DATOC	DATOC	DATOC
49						FETCH	FETCH
50	CLR	CLR	CLR	CLR	CLR	CLR	CLR
51	MEM 11	MEM 11	MEM 11	MEM 11	MEM 11	MEM 11	MEM11
52	STRT	STRT	STRT	STRT	STRT	STRT	STRT
53	MEM 9	MEM 9	MEM 9	MEM 9	MEM 9	MEM 9	MEM9
54	DATIC	DATIC	DATIC	DATIC	DATIC	DATIC	DATIC
55	MEM 8	MEM 8	MEM 8	MEM 8	MEM 8	MEM 8	MEM8
56	DATOB	DATOB	DATOB	DATOB	DATOB	DATOB	DATOB
57							
58	DATOA	DATOA	DATOA	DATOA	DATOA	DATOA	DATOA
59							
60	DCHA	DCHA	DCHA	DCHA	DCHA	DCHA	DCHA
61							
62	DS4	DS4	DS4	DS4	DS4	DS4	DS4
63							
64	DS5	DS5	DS5	DS5	DS5	DS5	DS5
65							
66	DS2	DS2	DS2	DS2	DS2	DS2	DS2
67							
68	DS1	DS1	DS1	DS1	DS1	DS1	DS1
69							
70	IORST	IORST	IORST	IORST	IORST	IORST	IORST
71							
72	DS0	DS0	DS0	DS0	DS0	DS0	DS0
73						LOAD IR	LOAD IR
74	IOPLS	IOPLS	IOPLS	IOPLS	IOPLS	IOPLS	IOPLS
75							
76							
77						LOAD AC	LOAD AC
78							
79							
80	SELD	SELD	SELD	SELD	SELD	SELD	SELD
81							
82	SELB	SELB	SELB	SELB	SELB	SELB	SELB
83							
84							
85						FORCE LOAD IR	FORCE LOAD IR
86							
87						MD SEL 1	MD SEL 1
88							
89						WAS JSR	WAS JSR
90							
91						DIV	DIV
92						TEST	TEST
93	DCHP OUT	DCHP OUT	DCHP OUT	DCHP OUT	DCHP OUT	DCHP OUT	
94	DCHP IN	DCHP IN	DCHP IN	DCHP IN	DCHP IN	DCHP IN	
95	INTP OUT	INTP OUT	INTP OUT	INTP OUT	INTP OUT	INTP OUT	
96	INTP IN	INTP IN	INTP IN	INTP IN	INTP IN	INTP IN	
97	+5V	+5V	+5V	+5V	+5V	+5V	+5V
98	+5V	+5V	+5V	+5V	+5V	+5V	+5V
99	GND	GND	GND	GND	GND	GND	GND
100	GND	GND	GND	GND	GND	GND	GND

B

Pin	7	6	5	4	3	2	1 (CPU)
1	GND	GND	GND	GND	GND	GND	GND
2	GND	GND	GND	GND	GND	GND	GND
3	+5V	+5V	+5V	+5V	+5V	+5V	+5V
4	+5V	+5V	+5V	+5V	+5V	+5V	+5V
5	MBO11	MBO11	MBO11	MBO11	MBO11	MBO11	MBO11
6						WHOA	WHOA
7	MA LOAD	MA LOAD	MA LOAD	MA LOAD	MA LOAD	MA LOAD	MA LOAD
8	MBO1Ø	MBO1Ø	MBO1Ø	MBO1Ø	MBO1Ø	MBO1Ø	MBO1Ø
9	MBO9	MBO9	MBO9	MBO9	MBO9	MBO9	MBO9
10	DMA LOAD	DMA LOAD	DMA LOAD	DMA LOAD	DMA LOAD	DMA LOAD	
11							
12	MBO8	MBO8	MBO8	MBO8	MBO8	MBO8	MBO8
13							
14	MBO7	MBO7	MBO7	MBO7	MBO7	MBO7	MBO7
15							
16	MBO6	MBO6	MBO6	MBO6	MBO6	MBO6	MBO6
17	DCHMO	DCHMO	DCHMO	DCHMO	DCHMO	DCHMO	DCHMO
18	MEM 15	MEM 15	MEM 15	MEM 15	MEM 15	MEM 15	MEM15
19							
20	STROBE	STROBE	STROBE	STROBE	STROBE	STROBE	STROBE
21	DCHMI	DCHMI	DCHMI	DCHMI	DCHMI	DCHMI	DCHMI
22	MEM 6	MEM 6	MEM 6	MEM 6	MEM 6	MEM 6	MEM6
23							
24	MEM 7	MEM 7	MEM 7	MEM 7	MEM 7	MEM 7	MEM7
25							
26	MEM 5	MEM 5	MEM 5	MEM 5	MEM 5	MEM 5	MEM5
27							
28	MEM 4	MEM 4	MEM 4	MEM 4	MEM 4	MEM 4	MEM4
29	INTR	INTR	INTR	INTR	INTR	INTR	INTR
30	INHIBIT (1)	INHIBIT (1)	INHIBIT (1)	INHIBIT (1)	INHIBIT (1)	INHIBIT (1)	INHIBIT
31							
32	MBO5	MBO5	MBO5	MBO5	MBO5	MBO5	MBO5
33	DCHO	DCHO	DCHO	DCHO	DCHO	DCHO	DCHO
34							
35	DCHR	DCHR	DCHR	DCHR	DCHR	DCHR	DCHR
36							
37	DCHI	DCHI	DCHI	DCHI	DCHI	DCHI	DCHI
38							
39	OV FLO	OV FLO	OV FLO	OV FLO	OV FLO	OVFLO	OVFLO
40							
41	RQENB	RQENB	RQENB	RQENB	RQENB	RQENB	RQENB
42	MBO4	MBO4	MBO4	MBO4	MBO4	MBO4	MBO4
43	MBO3	MBO3	MBO3	MBO3	MBO3	MBO3	MBO3
44	MBO2	MBO2	MBO2	MBO2	MBO2	MBO2	MBO2
45	INHIBIT TRANSMISSION	INHIBIT TRANSMISSION	INHIBIT TRANSMISSION	INHIBIT TRANSMISSION	INHIBIT TRANSMISSION	INHIBIT TRANSMISSION	INHIBIT TRANSMISSION
46	+V MEM	+V MEM	+VMEM	+V MEM	+V MEM	+V MEM	
47	MEM 2	MEM 2	MEM 2	MEM 2	MEM 2	MEM 2	MEM 2
48						CLK FLOP	CLK FLOP
49						EXT LOAD	EXT LOAD
50	GND	GND	GND	GND	GND	GND	GND
51							
52							
53							
54							
55	DATA 7	DATA 7	DATA 7	DATA 7	DATA 7	DATA 7	DATA 7
56	DATA 14	DATA 14	DATA 14	DATA 14	DATA 14	DATA 14	DATA 14
57	DATA 5	DATA 5	DATA 5	DATA 5	DATA 5	DATA 5	DATA 5
58	DATA 11	DATA 11	DATA 11	DATA 11	DATA 11	DATA 11	DATA 11
59	DATA 12	DATA 12	DATA 12	DATA 12	DATA 12	DATA 12	DATA 12
60	DATA 8	DATA 8	DATA 8	DATA 8	DATA 8	DATA 8	DATA 8
61	DATA 4	DATA 4	DATA 4	DATA 4	DATA 4	DATA 4	DATA 4
62	DATA Ø	DATA Ø	DATA Ø	DATA Ø	DATA Ø	DATA Ø	DATA Ø
63	DATA 9	DATA 9	DATA 9	DATA 9	DATA 9	DATA 9	DATA 9
64	DATA 13	DATA 13	DATA 13	DATA 13	DATA 13	DATA 13	DATA 13
65	DATA 1	DATA 1	DATA 1	DATA 1	DATA 1	DATA 1	DATA 1
66	DATA 15	DATA 15	DATA 15	DATA 15	DATA 15	DATA 15	DATA 15
67							
68	MEM 3	MEM 3	MEM 3	MEM 3	MEM 3	MEM 3	MEM3
69						SKIP	SKIP
70	MEM 1	MEM 1	MEM 1	MEM 1	MEM 1	MEM 1	MEM1
71	MEMØ	MEMØ	MEMØ	MEMØ	MEMØ	MEMØ	MEMØ
72	RELOAD DISABLE	RELOAD DISABLE	RELOAD DISABLE	RELOAD DISABLE	RELOAD DISABLE	RELOAD DISABLE	
73	DATA 3	DATA 3	DATA 3	DATA 3	DATA 3	DATA 3	DATA 3
74	MB LOAD (1)	MB LOAD (1)	MB LOAD (1)	MB LOAD (1)	MB LOAD (1)	MB LOAD (1)	MB LOAD
75	DATA 10	DATA 10	DATA 10	DATA 10	DATA 10	DATA 10	DATA 10
76	MEM 14	MEM 14	MEM 14	MEM 14	MEM 14	MEM 14	MEM 14
77	MBO1	MBO1	MBO1	MBO1	MBO1	MBO1	MBO1
78	ROS	ROS	ROS	ROS	ROS	ROS	
79	MBOØ	MBOØ	MBOØ	MBOØ	MBOØ	MBOØ	MBOØ
80	EXTERNAL SELECT	EXTERNAL SELECT	EXTERNAL SELECT	EXTERNAL SELECT	EXTERNAL SELECT	EXTERNAL SELECT	
81	-5V	-5V	-5V	-5V	-5V	-5V	
82	DATA 2	DATA 2	DATA 2	DATA 2	DATA 2	DATA 2	DATA 2
83	READ I/O	READ I/O	READ I/O	READ I/O	READ I/O	READ I/O	READ IO
84	+V INH	+V INH	+V INH	+V INH	+V INH	+V INH	
85	INHIBIT SELECT	INHIBIT SELECT	INHIBIT SELECT	INHIBIT SELECT	INHIBIT SELECT	INHIBIT SELECT	
86	MB CLEAR	MB CLEAR	MBCLEAR	MB CLEAR	MBCLEAR	MB CLEAR	MB CLEAR
87	READ 1	READ 1	READ 1	READ 1	READ 1	READ 1	READ 1
88							DRIVE IO
89	GND	GND	GND	GND	GND	GND	
90	READ 2	READ 2	READ 2	READ 2	READ 2	READ 2	READ 2
91						SHIFT 3	SHIFT 3
92	GND	GND	GND	GND	GND	GND	
93						SHIFT 2	SHIFT 2
94						SHIFT Ø	SHIFT Ø
95	DATA 6	DATA 6	DATA 6	DATA 6	DATA 6	DATA 6	DATA 6
96						SHIFT 1	SHIFT 1
97	+5V	+5V	+5V	+5V	+5V	+5V	+5V
98	+5V	+5V	+5V	+5V	+5V	+5V	+5V
99	GND	GND	GND	GND	GND	GND	GND
100	GND	GND	GND	GND	GND	GND	GND

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PARTS TO BE FREE FROM BURRS BREAK ALL EDGES .010

UNLESS OTHERWISE SPECIFIED - ALL MACH. SURFACES ✓

MATERIAL

FINISH

QTY.	DESCRIPTION	PART NO.

DRAWN	[signature]	3/13/70
CHECKED	[signature]	3/1/70
ENGINEER	R. Gruen	4/23/70
APPROVED	[signature]	5/18/70
FIRST USED ON		
SCALE	——	

DATA GENERAL CORPORATION
SOUTHBORO, MASSACHUSETTS 01772

TITLE
BACK PANEL NOVA 1200

SIZE	CODE	DRAWING NUMBER	REV
D	001	000090	03

SHEET 1 OF 1