



Data General Corporation, Westboro, Massachusetts 01580

Customer Documentation

Technical Notice:
Setting Jumpers on the
Model 7418A
VDA/255A Host Adapter

014-002385-00

Copyright ©Data General Corporation, 1994
All Rights Reserved
Printed in the United States of America
Rev. 00, September, 1994
Ordering No. 014-002385

Notice

DATA GENERAL CORPORATION (DGC) HAS PREPARED THIS DOCUMENT FOR USE BY DGC PERSONNEL, CUSTOMERS, AND PROSPECTIVE CUSTOMERS. THE INFORMATION CONTAINED HEREIN SHALL NOT BE REPRODUCED IN WHOLE OR IN PART WITHOUT DGC'S PRIOR WRITTEN APPROVAL.

DGC reserves the right to make changes in specifications and other information contained in this document without prior notice, and the reader should in all cases consult DGC to determine whether any such changes have been made.

THE TERMS AND CONDITIONS GOVERNING THE SALE OF DGC HARDWARE PRODUCTS AND THE LICENSING OF DGC SOFTWARE CONSIST SOLELY OF THOSE SET FORTH IN THE WRITTEN CONTRACTS BETWEEN DGC AND ITS CUSTOMERS. NO REPRESENTATION OR OTHER AFFIRMATION OF FACT CONTAINED IN THIS DOCUMENT INCLUDING BUT NOT LIMITED TO STATEMENTS REGARDING CAPACITY, RESPONSE-TIME PERFORMANCE, SUITABILITY FOR USE OR PERFORMANCE OF PRODUCTS DESCRIBED HEREIN SHALL BE DEEMED TO BE A WARRANTY BY DGC FOR ANY PURPOSE, OR GIVE RISE TO ANY LIABILITY OF DGC WHATSOEVER.

IN NO EVENT SHALL DGC BE LIABLE FOR ANY INCIDENTAL, INDIRECT, SPECIAL OR CONSEQUENTIAL DAMAGES WHATSOEVER (INCLUDING BUT NOT LIMITED TO LOST PROFITS) ARISING OUT OF OR RELATED TO THIS DOCUMENT OR THE INFORMATION CONTAINED IN IT, EVEN IF DGC HAS BEEN ADVISED, KNEW OR SHOULD HAVE KNOWN OF THE POSSIBILITY OF SUCH DAMAGES.

AV Object Office, AV Office, AViiON, CEO, CLARiiON, DASHER, DATAPREP, DESKTOP GENERATION, ECLIPSE, ECLIPSE MV/4000, ECLIPSE MV/6000, ECLIPSE MV/8000, GENAP, INFOS, microNOVA, NOVA, OpenMAC, PRESENT, PROXI, SWAT, TRENDVIEW, and WALKABOUT are U.S. registered trademarks of Data General Corporation; and **AOSMAGIC, AOS/VSMAGIC, AROSE/PC, ArrayPlus, AV Image, AV Imagizer Toolkit, AV SysScope, BaseLink, BusiGEN, BusiPEN, BusiTEXT, CEO Connection, CEO Connection/LAN, CEO Drawing Board, CEO DXA, CEO Light, CEO MAILL, CEO Object Office, CEO PXA, CEO Wordview, CEOwrite, COBOL/SMART, COMPUCALC, CSMAGIC, DATA GENERAL/One, DESKTOP/UX, DG/500, DG/AROSE, DGConnect, DG/DBUS, DG/Fontstyles, DG/GATE, DG/GEO, DG/HEO, DG/L, DG/LIBRARY, DG/UX, DG/ViiSION, DG/XAP, ECLIPSE MV/1000, ECLIPSE MV/1400, ECLIPSE MV/2000, ECLIPSE MV/2500, ECLIPSE MV/3200, ECLIPSE MV/3500, ECLIPSE MV/3600, ECLIPSE MV/5000, ECLIPSE MV/5500, ECLIPSE MV/5600, ECLIPSE MV/7800, ECLIPSE MV/9300, ECLIPSE MV/9500, ECLIPSE MV/9600, ECLIPSE MV/10000, ECLIPSE MV/15000, ECLIPSE MV/18000, ECLIPSE MV/20000, ECLIPSE MV/25000, ECLIPSE MV/30000, ECLIPSE MV/35000, ECLIPSE MV/40000, ECLIPSE MV/60000, FORMA-TEXT, GATEKEEPER, GDC/1000, GDC/2400, Intellibook, microECLIPSE, microMV, MV/UX, OpStar, PC Liaison, RASS, REV-UP, SLATE, SPARE MAIL, SUPPORT MANAGER, TEO, TEO/3D, TEO/Electronics, TURBO/4, UNITE, and XODIAC** are trademarks of Data General Corporation. **AV/Alert** is a service mark of Data General Corporation.

Technical Notice: Setting jumpers on the Model 7418A VDA/255A Host Adapter
014-002385-00

Revision History:

Original Release - September, 1994

Setting Jumpers on the Model 7418A VDA/255A Host Adapter

Before installing the VDA/255A host adapter board in your computer system, read the *Setting Up and Installing VMEbus Options in AViiON® Systems* (014-001867) manual that you received with your AViiON computer. It provides system configuration planning information that explains how to choose a number for your board. Note that if you have an earlier version of manual 014-001867 that did not have installation instructions for the VDA/255 host adapter, follow the instructions for the VDA/128 host adapter. Also, for power calculations, the VDA/255 host adapter requires 5.0 amperes of +5 V dc and .05 amperes of -12 V dc. The host adapter does not require +12 V dc.

Once you have read the configuration planning information in manual 014-001867, follow the steps below to make sure the VDA/255A host adapter's jumpers are set properly for the board number you have chosen. (Do not set the jumpers according to the instructions in manual 014-001867).

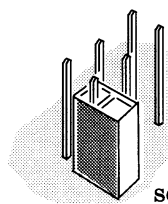
CAUTION: *To avoid electrostatic discharge damage to your equipment, read the ESD precautions section in the manual Setting Up and Installing VMEbus Options in AViiON® Systems before unpacking and handling the VDA/255A host adapter.*

To install or remove jumpers or to verify the jumper settings, do the following:

1. If the computer is running DG/UX System 5.4 Release 2.00 or a later revision, refer to Table 1 for the jumper settings. If it is running DG/UX 5.4 Release 1.00 or an earlier revision, refer to Table 2 for the jumper settings.

IMPORTANT: DG/UX Release 4.3.1. operating system or earlier revisions do not support the VDA/255A host adapter and Model 030 cluster controller.

2. Using the appropriate table, follow down the column of the board number that you have chosen, and if necessary, install (*in*) or remove (*out*) the E1, E2, and E20 jumpers shown in the boxes.



We provided you with several extra jumpers. We installed each on one pin of a jumper pin set. (See illustration.) If you have extra jumpers when you finish, store them on the board in the same way for future use. Note also that Table 3 contains the settings for the rest of the jumpers on the board. The factory sets these jumpers the same for all board numbers, and they do not require changing. We provided the settings in case a jumper falls off, or you accidentally remove one.

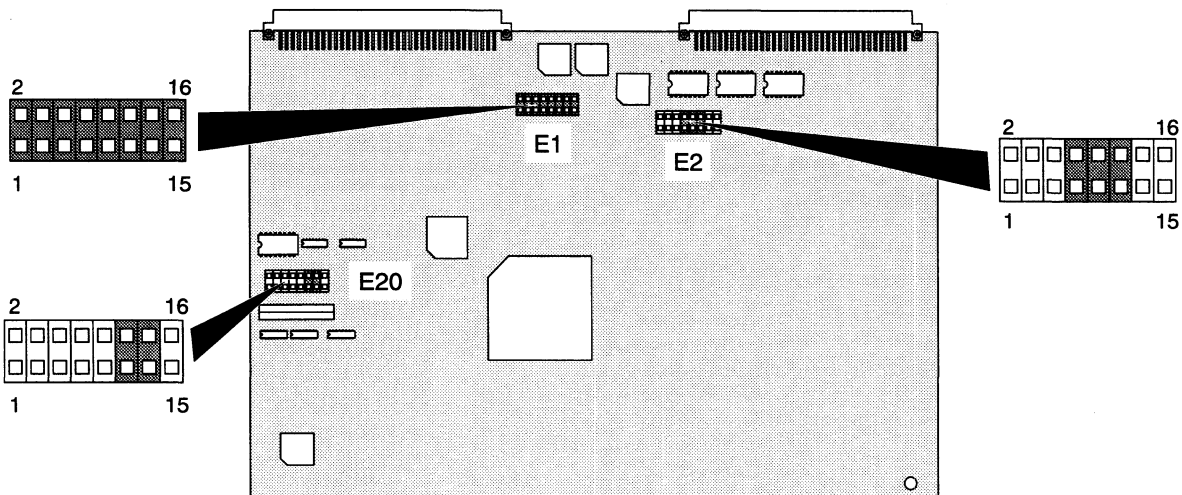
3. Once the board number is set, follow the instructions in the manual, *Setting Up and Installing VMEbus Options in AViiON® Systems* (014-001867) to complete the installation of the VDA/255A host adapter in your AViiON computer.

Table 1 – Settings for Jumpers E1, E2, and E20 when the computer is running DG/UX System 5.4 Release 2.00 or a later revision.

Jumper	Pins	Board Number								Function
		0	1	2	3	4	5	6	7	
E1	1-2	In	In	In	In	In	In	In	In	Address bit A23
	3-4	In	In	In	In	In	In	In	In	Address bit A22
	5-6	In	In	In	In	In	In	In	In	Address bit A21
	7-8	In	In	In	In	In	In	In	In	Address bit A20
	9-10	In	In	In	In	Out	Out	Out	Out	Address bit A19
	11-12	In	In	Out	Out	In	In	Out	Out	Address bit A18
	13-14	In	Out	In	Out	In	Out	In	Out	Address bit A17
	15-16	In	In	In	In	In	In	In	In	Address bit A16
E2	1-2	Out	Out	Out	Out	Out	Out	Out	Out	Address bit A31
	3-4	Out	Out	Out	Out	Out	Out	Out	Out	Address bit A30
	5-6	Out	Out	Out	Out	Out	Out	Out	Out	Address bit A29
	7-8	In	In	In	In	In	In	In	In	Address bit A28
	9-10	In	In	In	In	In	In	In	In	Address bit A27
	11-12	In	In	In	In	In	In	In	In	Address bit A26
	13-14	Out	Out	Out	Out	Out	Out	Out	Out	Address bit A25
	15-16	Out	Out	Out	Out	Out	Out	Out	Out	Address bit A24
E20	1-2	Out	In	Out	In	Out	In	Out	In	Status/ID bit D0
	3-4	Out	Out	In	In	Out	Out	In	In	Status/ID bit D1
	5-6	Out	Out	Out	Out	In	In	In	In	Status/ID bit D2
	7-8	Out	Out	Out	Out	Out	Out	Out	Out	Status/ID bit D3
	9-10	Out	Out	Out	Out	Out	Out	Out	Out	Status/ID bit D4
	11-12	In	In	In	In	In	In	In	In	Status/ID bit D5
	13-14	In	In	In	In	In	In	In	In	Status/ID bit D6
	15-16	Out	Out	Out	Out	Out	Out	Out	Out	Status/ID bit D7

Notation
 In
 Out
 In or Out

Indicates
 Jumper installed
 Jumper removed
 Jumpers that you must install (*In*) or remove (*Out*) in the field.
 All other jumpers are factory set as indicated.

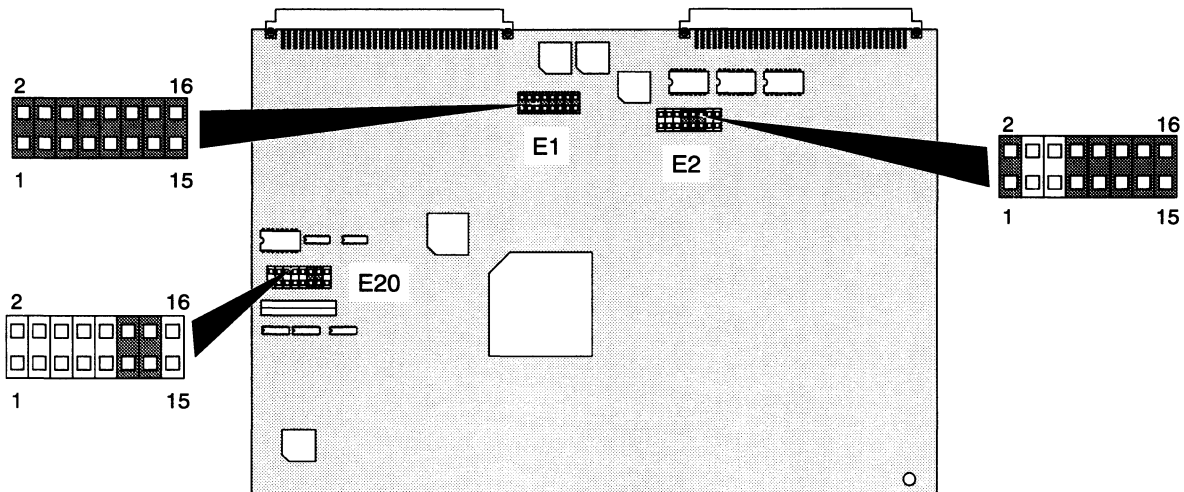


E1, E2, and E20 jumper locations (shown jumpered for board 0, DG/UX System 5.4 Release 2.00)

Table 2 – Settings for Jumpers E1, E2, and E20 when the computer is running DG/UX System 5.4 Release 1.00 or an earlier revision

Jumper Pins	Board Number								Function	
	0	1	2	3	4	5	6	7		
E1	1–2	In	In	In	In	In	In	In	In	Address bit A23
	3–4	In	In	In	In	In	In	In	In	Address bit A22
	5–6	In	In	In	In	In	In	In	In	Address bit A21
	7–8	In	In	In	In	In	In	In	In	Address bit A20
	9–10	In	In	In	In	Out	Out	Out	Out	Address bit A19
	11–12	In	In	Out	Out	In	In	Out	Out	Address bit A18
	13–14	In	Out	In	Out	In	Out	In	Out	Address bit A17
E2	15–16	In	In	In	In	In	In	In	In	Address bit A16
	1–2	In	In	In	In	In	Out	Out	Out	Address bit A31
	3–4	Out	Out	Out	Out	Out	Out	Out	Out	Address bit A30
	5–6	Out	Out	Out	Out	Out	Out	Out	Out	Address bit A29
	7–8	In	In	In	In	In	In	In	In	Address bit A28
	9–10	In	In	In	In	In	In	In	In	Address bit A27
	11–12	In	In	In	In	In	In	In	In	Address bit A26
E20	13–14	In	In	In	In	In	Out	Out	Out	Address bit A25
	15–16	In	In	In	In	In	Out	Out	Out	Address bit A24
	1–2	Out	In	Out	In	Out	In	Out	In	Status/ID bit D0
E20	3–4	Out	Out	In	In	Out	Out	In	In	Status/ID bit D1
	5–6	Out	Out	Out	Out	In	In	In	In	Status/ID bit D2
	7–8	Out	Out	Out	Out	Out	Out	Out	Out	Status/ID bit D3
	9–10	Out	Out	Out	Out	Out	Out	Out	Out	Status/ID bit D4
	11–12	In	In	In	In	In	In	In	In	Status/ID bit D5
	13–14	In	In	In	In	In	In	In	In	Status/ID bit D6
	15–16	Out	Out	Out	Out	Out	Out	Out	Out	Status/ID bit D7

<p>Notation</p> <p>In</p> <p>Out</p> <p>In or Out</p>	<p>Indicates</p> <p>Jumper installed</p> <p>Jumper removed</p> <p>Jumpers that you must install (<i>In</i>) or remove (<i>Out</i>) in the field.</p> <p>All other jumpers are factory set as indicated.</p>
--	--



E1, E2, and E20 jumper locations (shown jumpered for board 0, DG/UX System 5.4 Release 1.00)

Table 3 – Remaining jumper settings (board numbers 0–7)

Jumper	Pins	Status	Function
E3	2–3	In	Bus grant 0
E4	2–3	In	Bus grant 1
E5	2–3	In	Bus grant 2
E6	1–2	In	Bus grant 3
	3–4	In	Bus grant 3 (selected)
E7		Out	Bus request Level 0
E8		Out	Bus request Level 1
E9		Out	Bus request Level 2
E10		In	Bus request Level 3 (selected)
E11	1–2	Out	Interrupt request 1
	3–4	Out	Interrupt request 2
	5–6	Out	Interrupt request 3
	7–8	In	Interrupt request 4 (selected)
	9–10	Out	Interrupt request 5
	11–12	Out	Interrupt request 6
	13–14	Out	Interrupt request 7
	15–16	Out	Not used
E12	1–2	Out	PROM size 27512
	2–3	In	PROM size 27256
E13		In	DP RAM program space
E14		In	DP RAM data space
E16		Out	DP RAM standard space
E17		In	DP RAM supervisor space
E18		In	Watchdog timer
E19		Out	SYSFAIL
E22	1–2	Out	Factory reserved
	3–4	In	No read/write test
	5–6	Out	Reserved for future use
	7–8	Out	Test/clear static RAM
	9–10	Out	Clear RAMs
	11–12	Out	Reserved for future use
	13–14	In	32-bit long words
	15–16	Out	Reserved for future use
E23		In	Address bit A15
E24		In	Address bit A14
E25		Out	Interrupt acknowledge
E26		In	Interrupt acknowledge
E27		In	Interrupt acknowledge
E32	1–2	In	Transport node add bit 7
	3–4	In	Transport node add bit 6
	5–6	In	Transport node add bit 5
	7–8	In	Transport node add bit 4
	9–10	In	Transport node add bit 3
	11–12	In	Transport node add bit 2
	13–14	In	Transport node add bit 1
	15–16	In	Transport node add bit 0
E33		In	Terminate coaxial cable at host adapter
E34		Out	Samples BGxIn to prevent false bus grant
E35		Out	Reserved for future use
E36		Out	Reserved for future use
Notation		Indicates	
In		Jumper installed.	
Out		Jumper removed.	

End of Notice